

FIG. 2

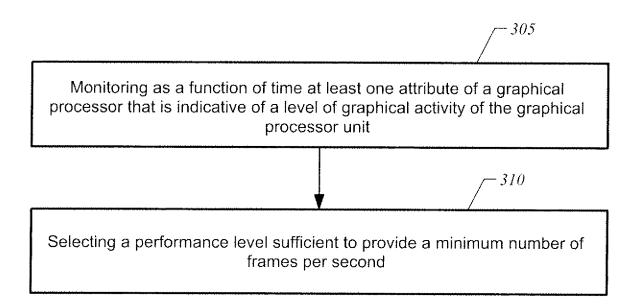


FIG. 3

Monitoring a first parameter indicative of a utilization factor of the core clock domain and a second parameter indicative of a utilization factor of the memory clock domain

-410

Based on the values of the first parameter and the second parameter,

FIG. 4

determining a performance level sufficient to provide a minimum number of frames per second

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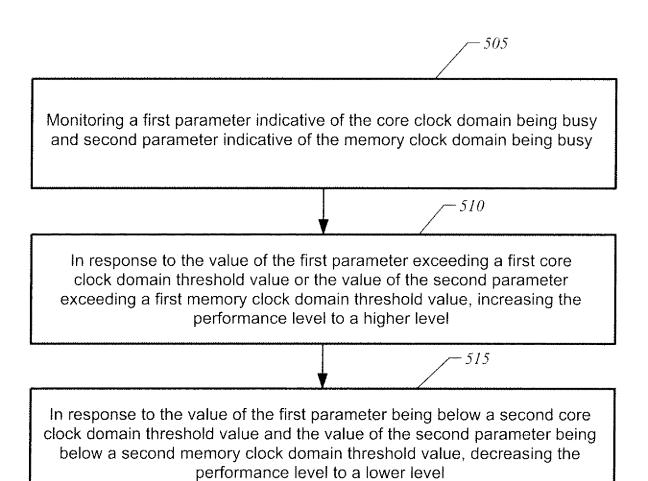


FIG. 5